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20792 7590 01/20/2004

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Candi L. Riggs

(Depositor's name)

Candi L. Riggs

(Signature)

March 29, 2004

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/923,670	03/07/2001	Hag-ju Cho	5649-877	1538

TITLE OF INVENTION: METHOD OF FABRICATING INTEGRATED CIRCUIT DEVICES HAVING DIELECTRIC REGIONS PROTECTED WITH MULTI-LAYER INSULATION STRUCTURES

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	04/20/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
THOMAS, TONIAE M	2822	438-003000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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(A) NAME OF ASSIGNEE

Samsung Electronics Co., Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Republic of Korea

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) Robert M. Meeks #40,723 (Date) 3/29/04

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